

## **Biodata**

**Name:** Dr. Mrs. Saroj Shankar Hole

**Birth Date:-**11 April 1965

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**Category:** Open

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### **Academic Qualifications :**

Exam. Passed	Year of Passing	Board/University	% of Marks Obtained
S.S.C.	1981	Maharashtra State	86%
H.S.C.	1983	Maharashtra State	65%
B.Sc.	1986	Nagpur	70%
M.Sc.	1988	Amravati	68%
Ph.D	2016	Amravati	

### **Other Qualifications:**

- Certificate Course in **Solar systems and its Installation**
- Training Program in Computer awareness

### **Software Proficiency:**

- **Assembly Language Programming** for various **Architectures** used :
- **C language Programming**

- PCB design software: EAGLE-3, EXPRESS PCB-7, SMART Layout

### **Architectures used :**

- 8085 Microprocessors
- Arduino platform

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### **Professional Information :**

- **Teaching Experience:** 25 years as on 1<sup>st</sup> July 2018
  - Modern College of Arts ,Science and Commerce, Ganeshkhind, Pune
- **Subjects Taught:**
  - Principals/ Fundamentals of Analog Electronics
  - Principles/Fundamentals of DIGITAL Electronics ,
  - Analog and Digital System Design
  - Communication Principles
  - Computer Organisation and Microprocessor Architecture
  - 8051 programming and Embedded Systems
  - 8051 Architecture, programming ,and Interfacing
  - Operational Amplifiers
  - Industrial Electronics
  - Practicles of FYBSc,SYBSc,TYBSc, FYBCS,SYBCS

### **New methods introduced in teaching:**

- **Chapter and topic wise remedial coaching**
- **Teaching through labtop and software (few lectures)**
- **Assignments given on whats up group of students on every chapter after completion of chapter**

- For practicles I generally want that students should built the circuits by their own on breadboards instead of readymade circuits given to them . Because of this STUDENTS understand the things better.

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Completed one **Orientation program** and 2 **Refresher course** in Electronics.

**Research Project: Under DBT guidance is given to students for the project “Calling and Messaging unit using ARDUINO-UNO”**

**Examination Duties:**

**Junior Supervisor for University examination**

**External Senior Supervisor :In various colleges in Pune**

**Examiner : In theory as well as practicles**

- F.Y.B.Sc. Electronics and Computer Science.
- S.Y.B.Sc. Computer Science
- T.Y.B.Sc. Electronics

**Syllabus Design:**

- Career Oriented Course in Embedded Systems and Applications
- Hands on Training under DBT Star college Scheme and skill development courses on PCB design and Photovoltaic System design and Installation at own department.
- Syllabus design for FYBSc( Electronics)

**Organising Committee Member:**

- Organising committee member for Indian Science Academies' Lecture Workshop "Smart Materials and Future Technologies for Sustainable Development "
- Organising committee member for hands on training workshop on Interfacing Sensors to Arduino and their Applications" sanctioned by Board of Students' Development, SPPU, Pune.
- Organising committee member of One day state level workshop on **Role of Advanced Sensors in the development of Smart City.**
- Member of Local workshop on **Lab Safety** for Laboratory Staff from various colleges in Pune city.
- Member of various activities under DBT STAR college scheme

**Editorial Board Member for International Journal of NanoElectronics and Nanotechnology**

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